

ADT132K-V-RIS-ET7215 Process Dual Trip Amplifier

 Suitable for SIL 1, SIL 2 or SIL 3 rated (EN 61508-2) safety instrumented system (SIS) loop applications

• Supply voltage options: 115Vac ±20%

240Vac ±20% 24Vdc ±10% 48Vdc ±10%

- RFI Protection to EN 61000-4-3:2006/A2:2010
- AMELEC Standard 10 year warranty
- Digital Indication available (DI option)
- 24Vdc @ 22mA two wire I/P loop excitation available ('E' option)
- Non-Smart/Non-uProcessor based, Type 'A' instrument

Technical Specifications

Input

Any current or voltage (DC) drive that can be terminated in a PI network to produce a 400mV span. Typically: 0-1mA, 0-10mA, 4-20mA, 0-5V, 0.4-2.0V, 0-10V, 0-100Vdc

Output Relay Contacts

2-Pairs of either N.O or N.C contacts for each set point, rated 3A 240Vac resistive load as standard. Set point adjustments via 15-turn blindset potentiometers.

Deadband

Adjustable Hysteresis from 0.5 to 20% of span for each trip set point, set via 15-turn blindset potentiometers.

Performance

Trip settability: ±1%
Trip repeatability: ±0.1%

Response time: <100mS (0-100% input step change)
Isolation: 500Vdc Input/Contacts/Contacts/Supply/Earth
Monitor Points: 0-5Vdc representing 0-100% of input Span
Open circuit response: Downscale drive as standard
(O/C response Upscale drive option available on request)

Environmental Conditions

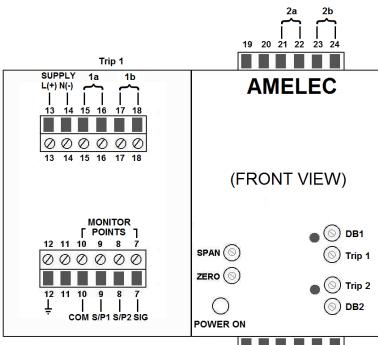
Storage Temperature: -40 to 70°C
Operating Ambient: -15 to 55°C
Relative Humidity: 5 – 95% RH
EMC: 2014/30/EU, EN 61326-1:2013
(Generic Industrial levels)

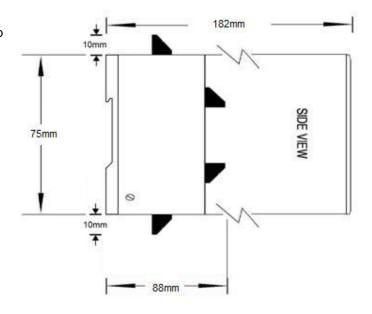
Customer Termination

Fixed screw terminals as standard (Plug-in screw terminal blocks option available)

Wiring

Trip 2





Mounting / Dimensions:

Heavy duty Din Rail (TS35) as standard, Enclosure overall Dims: 100w x 75h x 182d mm. (Surface by Seismic keyhole plate option available, Rear Mounting Plate Dims: 100w x 130h mm).

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