

ADT131LT Process Trip Amplifier with Latch & Time Delay

- Suitable for SIL 1, 2 or 3 applications
- Suitable for any process input
- Supply voltages: 115Vac $\pm 20\%$
240Vac $\pm 20\%$
24Vdc $\pm 2.5V$
48Vdc $\pm 5V$
- RFI Protection to IEC61000-4-3 available
- AMELEC Standard 10 year warranty
- Digital Indication available

Technical Specifications

Input

Any current or voltage drive that can be terminated in a PI network to produce a 400mV span. Input O/C drive downscale.

Delay

Delayed trip action with automatic timer rest if the signal recovers before the timer times out. Delay period set by 15 turn potentiometer.

Latch/Reset

Latching trip action. Once the unit goes into trip, it remains in trip until it is reset by applying contact closure across TB 5 & 6.

Trip

Trip can be specified as High or Low, set by 15 turn blindset potentiometer.

Output

1 x N.O. & 1 x N.C. 250Vac 2A 100VA resistive

Isolation

1000V RMS* Input/Output/Supply/Earth
*(500Vdc if RFI option 'K' is specified)

Environmental Conditions

Storage Temperature: -40 to 70°C
Operating Ambient: -15 to 55°C
Relative Humidity: 5 – 95% RH

Dimensions

50w x 75h x 110d (mm)

WIRING

