

ADT131KTL Single Trip Amplifier with Latching Relay + Remote Reset Facility

Suitable for SIL 1 & SIL 2 rated (IEC61508-2) safety instrumented system (SIS) loop applications, as 1001 architecture (HFT:0)

Supply voltage options: 20Vac ±20%

115Vac ±20% 230Vac ±20% 24Vdc ±10% 48Vdc ±10%

RFI Protection to IEC61000-4-3:2006/A2:2010

24Vdc @22mA two-wire Input loop Excitation option 'M' available

Fixed or Variable Time Delay into Trip 'T' option available

Front fascia Digital Display 'DI' option available

Non-Smart / Non-uProcessor based, Type A instrument

AMELEC Standard 10 year warranty

Technical Specifications

Input

Any current or voltage drive that can be terminated in a PI network to produce a 400mV span.

Typical inputs: 4-20mA, 0-10mA, 1-5Vdc, 0-10Vdc or 0-100Vdc.

Output

A set of Latching Relay contacts (T1a=N.O, T1b=S.P.C.O), with a common Remote Contact Reset facility.

Changeover contacts, rated at 250VAC, 2A, 100VA resistive. Fail Safe Relay De-energises on Trip & on loss of power as std Red LED indication of relay status

(ON Energised/healthy, Extinguished in Trip/De-energised state)

Performance

Trip settability: ±1% Trip repeatability: ±0.1%

Isolation: 500Vdc Input/Contacts/Supply/Earth Response time: <100mS (0-100% input step change)

Deadband: 1% Span Hysteresis as std. (Variable hysteresis 0.5%-20% span 'V' option available) Input Open Circuit response: Downscale drive as std

(O/C Upscale drive available - 'X' option)

Consumption: <3VA

Environmental Conditions

Storage Temperature: -40 to +70°C Operating Ambient: -15 to +55°C

Relative Humidity: 5 – 95% RH (Non-Condensing) EMC: 2014/30/EU, EN 61326-1:2013 (Generic Industrial levels) RF Immunity: 20-3000MHz <10V/m, 80-1000MHz <30V/m,,

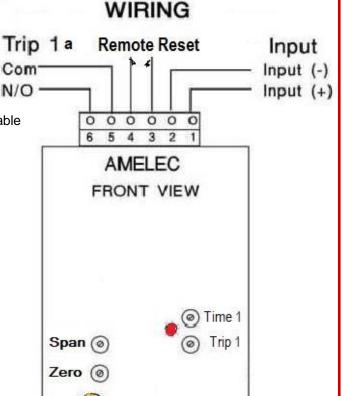
(889MHz/1.75GHz <40V/m)

Dimensions

50w x 75h x 182d mm

Mounting

Din Rail (TS35) or Surface by seismic Keyhole plate (50w x 130h) Tel: 01908-567003 Email: sales@amelec-uk.com Visit: www.amelec-uk.com Fax: 01908-566735 AMELEC Instruments, Cochran Close, Crownhill, Milton Keynes, MK8 0AJ Issue 3: 2018



10 11 12

Supply

Trip 1 a

Reset

Trip 1b

0 0

Latching Trip

Supply

Power on

7 8 9

N/C

Com

N/O

Input

Trip 1b

0 0