

# **ADT131DITX Trip with Timer & Display**

Suitable for any process input

Supply voltages: 115Vac ±20%

240Vac ±20% 24Vdc ±2.5V 48Vdc ±5V

AMELEC Standard 10 year warranty

## **Technical Specifications**

#### Input

Any current or voltage drive that can be terminated in a PI network to produce a 400mV span

#### **Trip**

1 x Trip can be specified as High or Low, fail safe or non-fail safe. Trip range 0-110% of input, set by 15 turn blind set potentiometer. Trip time delay is active on trip and rest is immediate.

### Timer Delav

Set by 15 turn blind set potentiometer in the range of 200 to 4000 seconds. Delay time can be monitored as frequency (Hz) on Test 1. Delay time = (1/Freq) x 8192. A link across TB7/8 will put time to fast to aid testing.

#### Display

Continuous display of the input signal on a 3½ LCD display. On operating push button the display will show the trip set point in the same scale. The display range can be specified in the range -1999 to +1999.

#### <u>Isolation</u>

1000V RMS. Input/Output/Contacts/Supply

#### **Performance**

Trip settability: ±1%
Trip repeatability: ±0.1%

#### **Environmental Conditions**

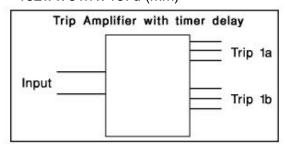
Storage Temperature: -40 to 70°C Operating Ambient: -15 to 55°C Relative Humidity: 5 – 95% RH

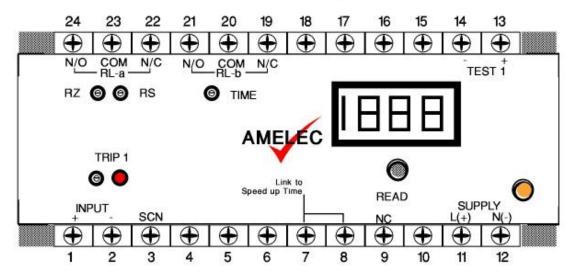
#### Outputs

Trip output is a D.P.C.O. contacts, rated at 250VAC, 2A, 100VA (resistive)

#### Dimensions

152w x 81h x 137d (mm)





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