

## ABM 733 Process Signal Dual Trip Transmitter

- Suitable for combinations of process inputs and outputs
- Supply voltage 24Vdc  $\pm$  10%
- Amelec standard 10 year guarantee
- Suitable for SIL Level 1, 2, & 3 (IEC 61508-2)

### TECHNICAL SPECIFICATION

#### FUNCTION

Process V / I input signal Converter / Isolator  
High Trip: Relay de-energise on rising input.  
Low Trip: Relay de-energise on falling input.

#### INPUT

DC current / voltage can be specified in the range of:  
Current up to 100mA max (Passive)  
Voltage 0.4 to 100V max  
Typical input: 4 – 20mA (Passive)

#### OUTPUT

DC current or voltage specified in the range of:  
Current up to 100mA max in Sink configuration (externally powered)  
Current up 22mA max Source configuration (Internally powered)  
Voltage any from 0.4 to 20V max @ up to 20mA.  
Typical output range: 4 - 20mA (Source)

The Trip output is a pair of changeover contacts DPCO per set point, rated at 250VAC, 2A, 100VA (resistive).

#### CONTROLS

Zero / Span: 15 turn potentiometers to calibrate Output.  
Set point: 15 turn potentiometer to set Trip point within set Input range.

#### INDICATOR

Amber Led: power ON indicator  
Red Led: Relay status indicators

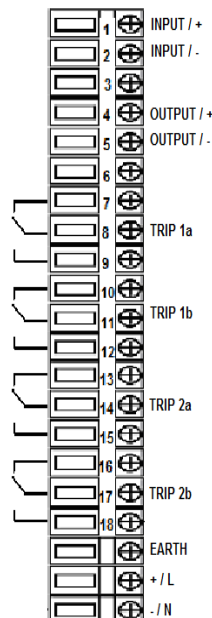
#### PERFORMANCE

Response time: Typically < 400mS  
Linearity : <  $\pm 0.1\%$   
Trip repeatability: <  $\pm 0.1\%$   
Trip settability: <  $\pm 0.1\%$

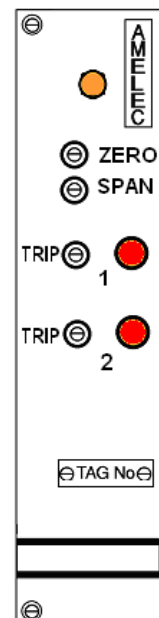
#### PROTECTION

Isolation 1000V RMS\*. Input/Contacts/Supply/Earth  
\*500VDC if RFI option (K) is specified.  
Internal Fuse.  
Fail safe on loss of power  
Input over range typically at 300%.  
Output Saturation 125%

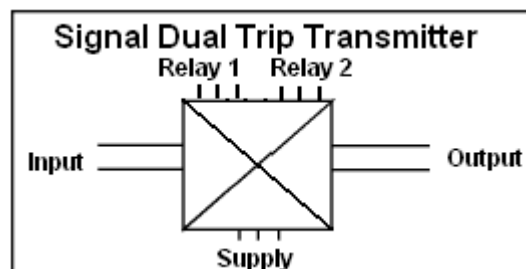
#### TERMINATION



#### FRONT VIEW



#### FUNCTION BLOCK DIAGRAM



#### ENVIROMENTAL CONDITION

Storage temperature: - 40 to +70 °C  
Operating Ambient: -15 to +55 °C  
Relative Humidity: 5 to 95% RH

#### MOUNTING / DIMENSION

Card 3U high 7HP wide  
Mounting 19" rack / 84E wide (See rack GA for details)  
Card weight < 200g

#### ADD ON / OPTIONS

DI: Common LCD display for local monitoring  
J : Input injection jack socket  
P: Test point (Trip set point monitoring)  
K: RFI protection to IEC801-3  
Non standard Power supply ranges available